Bachelor of Science (B.Sc.) Semester-VI (C.B.S.) Examination

MICRO-CONTROLLER

Paper—2

[Maximum Marks: 50 (Electronics) Time: Three Hours] N.B.: (1) All questions are compulsory and carry equal marks. (2) Draw neat diagrams wherever necessary. EITHER (A) (i) Explain the Register Banks of 8051 Micro-Controller. (ii) What are Special Function Registers? Explain the SFRS of 8051 Micro-Controller. 1. OR With suitable examples, explain the function of CY and OV flags. (B) (i) State the functions of the following pins: (ii) (a) ALE (b) EA (c) **PSEN** 5+5 (d) RST TDX. (e) 10 **EITHER** (A) Describe the interrupts and their handling in the 8051 Micro-controller. 2. With a suitable example, explain Base + Index Register - Indirect Addressing Mode OR Write a simple ALP to swap the lower and upper nibble of the accumulator data. (B) (i) 5+5 (ii) **EITHER** (A) Explain the function of the following instructions: 3. SJMP (ii) LJMP (iii) DJNZ (iv) CJNE (v) JMP @ A+DPTR Write a simple ALP to AND the bytes of R and R register and copy result in R register. 5+5 OR What is the need for subroutines? (B) (i) Explain ACALL instruction. 2+4+4(iii) Write a subroutine for delay. **EITHER** 10 (A) Explain ADC and DAC interfacing. 4. OR 10 (B) Explain 4 × 4 keyboard interfacing.

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- (A) Write one point of difference between CISC and RISC microcontrollers.
- (B) Give the difference between program and data memory.
- (C) What is the function of B-Register?
- (D) Give two examples of Boolean Variable Manipulation Instruction.
- (E) Give the difference between MOV and MOVX instructions.
- (F) Which mode of addressing is used for SFRs ?
- (G) What is the content of SP after executing both the instructions?

 MOV SP, # A2H

POP THO

- (H) Define subroutine Nesting.
- (I) What is the need of branching in a program?
- (J) What is the advantage of using LCD display over LED display?
- (K) Draw the bit functions of SCON register.
- (L) Define Baud rate.

1×10=10

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